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## CAREER OBJECTIVES

Senior Electrical Design Engineer with potential project management/leadership responsibilities

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## JOB-RELATED SKILLS

- Technical Languages: VHDL, Verilog, Assembly Languages, C, LabVIEW
- Design Tools: Mentor HDL Designer, ModelSim, Lattice ispLEVER, OrCAD, Microchip MPLAB, Arium Debugger, NI TestStand
- Digital Design with Xilinx, Altera, and Lattice PLDs and FPGAs, Microchip and 8051 microprocessors, Pentium M processors
- Document writing for government regulated work (FDA and FAA)

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## EMPLOYMENT EXPERIENCE

**edwardjpalmer, inc.** – Minneapolis, Minnesota      Engineering contracts with **Boston Scientific,**      01/2006 – Present  
**Rivertek Medical, Honeywell Aerospace**

- Designed programmable logic and embedded firmware in assembly language and C for medical product prototypes
- Assisted in part selection and circuit design for reworking existing analog and digital electronics
- Designed electronics and verification tests for elements of a GPS-based commercial aviation landing guidance system
- Utilized NI LabVIEW and TestStand for automating testing of medical device test systems
- Developed design requirements and test cases for verification testing for government approval (FDA and FAA)

**Aerotek** – Arden Hills, Minnesota      Engineering contracts with **Medtronic** and **Rivertek Medical**      03/2004 – 12/2005

- Used VHDL to program FPGAs for firmware/hardware-based implantable device test systems
- Designed and executed test protocols for device verification tests (DVT) and validation
- Programmed assembly language and C for embedded applications

**Berlitz Language Institute** – San José, Costa Rica      Instructor      02/2002 – 05/2002  
Director      06/2002 – 12/2003

- Administrated a staff of 30 and a student body of more than 500, including corporate clients
- Increased monthly lessons given by 26%, and decreased outstanding accounts receivable by 77%
- Communicated with company executives and staff about educational and financial goals, and needs of the institute
- Managed focus teams to retain current clients, increase customer satisfaction, and expand business

**Logic Product Development** – Minneapolis, Minnesota      Electrical Engineer      03/2001 – 09/2001

- Tested and verified on-board FPGAs within outside analog and digital constraints
- Designed, debugged, and modified VHDL and Verilog code and schematics for RF-based home monitoring device

**3M Company** – Saint Paul, Minnesota      Electrical Engineering Aide      09/1996 – 02/2001

- Programmed assembly language on microprocessors for use in RFID research project
- Designed and constructed a custom 40-channel mixed signal test fixture for experimental sample testing

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## EDUCATION

University of Minnesota – Twin Cities – Institute of Technology (August 2000)  
Bachelor of Science Electrical Engineering with emphasis in Digital Design and DSP (GPA: 3.05)

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## SPECIAL ACTIVITIES AND OTHER SKILLS

- Member of The Institute of Electrical and Electronics Engineers (IEEE), 1999 – Present
- Member of James J. Hill Business Reference Library, 2007 – Present
- Participant in LifeScience Alley (formerly Medical Alley), 2007 – Present
- Manager of the Medical Device Engineers Network, Minnesota subgroup on LinkedIn
- Spanish language proficiency (written and oral) in business and personal situations